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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/597,220	07/17/2006	Nicolaas Lambert	NL040022	7902
24737	7590	09/09/2008	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			VERBRUGGE, KEVIN	
P.O. BOX 3001				
BRIARCLIFF MANOR, NY 10510			ART UNIT	PAPER NUMBER
			2189	
			MAIL DATE	DELIVERY MODE
			09/09/2008	PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/597,220	LAMBERT ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	Kevin Verbrugge	2189

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 17 July 2006 and 06 June 2008.  
 2a) This action is **FINAL**.                            2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-11 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-11 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on 06 June 2008 is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)  
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  
 3) Information Disclosure Statement(s) (PTO/SB/08)  
 Paper No(s)/Mail Date \_\_\_\_\_.  
 4) Interview Summary (PTO-413)  
 Paper No(s)/Mail Date \_\_\_\_\_.  
 5) Notice of Informal Patent Application  
 6) Other: \_\_\_\_\_.

## DETAILED ACTION

### ***Specification***

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following title is suggested: METHOD OF INCREASING BOOT-UP SPEED BY PREFETCHING DATA FROM A LOG.

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 6,047,363 to Lewchuk.

Regarding claims 1, 6, and 11, Lewchuk shows the claimed computer system in Fig. 1, for example. He shows the claimed computing means as CPU 12. He shows the claimed data storing means as disk 16. He does not teach that his disk 16 includes a data caching means as claimed.

Official Notice is taken that it was well-known in the art at the time of the invention to include a cache in a disk drive unit for temporarily storing data being written to or read from the disk as claimed.

Lewchuk's device operates as claimed, making logs (profiles) and using them the next time a particular application is started. The profiles are consulted and the desired data is prefetched so as to improve performance by avoiding a miss in the processor's cache.

Lewchuk does not teach prefetching into the disk cache, as claimed, since Lewchuk does not disclose a disk cache, as mentioned above. Rather Lewchuk's device prefetches into one of the processor's caches (data cache, instruction cache, profile cache, or prefetch cache).

However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to include a cache in Lewchuk's disk 16 and to use his prefetch technique to prefetch data into that cache as well to further improve performance. The same technique of prefetching into the processor cache could be used to prefetch into a disk cache to stage the data that much closer to the processor. The skilled artisan would have easily recognized that Lewchuk's technique can be used to prefetch into any cache, and it would have been obvious to prefetch into a disk cache to improve the performance of the system.

Regarding claims 2 and 7, Lewchuk discloses the claimed heuristic approach at column 4, lines 46-53, for example, where he teaches that the relative success of a

particular profile in predicting needed data is monitored and if unsatisfactory, the profile may be replaced with a new profile which better matches the application's needs.

Regarding claims 3, 4, 8, and 9, Lewchuk teaches that his device can have several profiles and even can have several profiles for a single application, with a separate profile for each point of entry into the application.

Regarding claims 5 and 10, Lewchuk's disk 16 does not include the claimed cache or local computing means, but they would be obvious in light of the rest of his disclosure. The cache would be obvious as discussed above. The local computing means would be obvious as a means of controlling the cache.

### ***Conclusion***

The method claims are grouped and rejected with the apparatus claims because the steps of the method are met by the disclosure of the apparatus and methods of the reference(s) as discussed above.

Any inquiry concerning this Office action should be directed to the Examiner by phone at (571) 272-4214.

Any response to this Office action should be labeled appropriately (including serial number, Art Unit 2189, and type of response) and mailed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, hand-carried or delivered to the

Customer Service Window at the Randolph Building, 401 Dulany Street, Alexandria, VA 22313, or faxed to (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197.

/Kevin Verbrugge/

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Art Unit 2189